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We refer to this approach as Co-Packaged Optics (CPO) when applied to networking applications and Optical Compute Interconnect (OCI) when applied to compute fabrics

Intel paired one of its CPUs with an optical compute interconnect (OCI) chiplet.

First demonstrated at OFC 2024, the industry-first Marvell 3D SiPho Engine - which supports 200Gbps electrical and optical interfaces - is a fundamental building block for incorporating ...

The OCI chiplet combines a silicon photonics IC, which includes on-chip lasers and optical amplifiers, with an electrical IC. While the chiplet demonstrated was co-packaged with an Intel ...

The optical compute interconnect (OCI) chiplet can be attached to CPUs and GPUs to enable high bandwidth, low power consumption, and extended-reach I/O connectivity.

Intel Corporation's Integrated Photonics Solutions (IPS) Group has demonstrated the industry's first fully integrated bidirectional optical compute interconnect (OCI) chiplet co-packaged ...

MALTA, N.Y., May 4, 2026 - GlobalFoundries (Nasdaq: GFS) (GF) today announced the introduction of its SCALE(TM) optical module solution for co-packaged optics (CPO). GF's SCALE ...

At #OFC24, Intel demonstrated an advanced Optical Compute Interconnect (OCI) chiplet co-packaged with a prototype of a next-generation Intel CPU.

The optical compute interconnect (OCI) chiplet can be attached to CPUs and GPUs to enable high bandwidth, low power consumption, and ...

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Intel recently developed an optical chip interconnect system and demonstrated its first fully integrated optical I/O (OCI) chiplet. This chiplet addresses one of the biggest challenges facing ...

A co-packaged xPU (CPU, GPU, IPU) optical I/O solution can support higher bandwidths with high power efficiency, low latency, and longer reach, which is exactly what AI/ML infrastructure ...

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